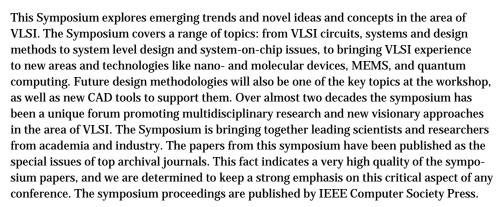
IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI



CALL FOR PAPERS

July 4-6, 2011 Chennai, India



Several leading scientists from newly emerging areas of nanoelectronics, MEMS and molecular, biological and quantum computing will be invited speakers at the symposium. The Symposium has established a reputation in bringing well-known international scientists as invited speakers, and this trend will continue.

Contributions are sought in the following areas

Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing. MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, Physical Design, Intellectual property creating and sharing.

The Symposium Program will include contributed papers and speakers invited by the Program Committee, as well as a poster session. The keynote addresses, panels and special sessions are planned as well. Authors should send a PDF version of their papers through the web based submission site, as described on the symposium website.

Paper submission deadlines

Important dates are:

Abstract Submission Deadline: February 20, February 25, 2011

Full Paper Submission Deadline: March 1, March 5, 2011 max six two-column

Acceptance Notification: March 27, 2011 Submission of Final Version: April 15, 2011











Website:

http://www.isvlsi2011.org/

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