

20th

IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI Call for Special Session Proposal

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ISVLSI 2022

July 4 – 6, 2022
Aliathon Resort,
Pafos, CYPRUS



The 2022 Symposium, explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design issues, to bringing VLSI design to new areas and technologies such as nano- and molecular devices, security, artificial intelligence, and Internet-of-Things, etc. Future design methodologies and new EDA tools are also a key topic at the symposium. Over three decades the ISVLSI has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry. The Symposium has established a reputation in bringing together well-known international scientists as invited speakers, and the 20th edition will not be the exception!

Call for Special Session Proposals

The ISVLSI 2022 is inviting proposals for special sessions that explore emerging trends and concepts covering a broad range of topics in the area of VLSI. In particular, special sessions (academic and industrial) concerning hot topics in VLSI, including those dealing with the introduction and discussion of emerging problems, or addressing recent technical trends in computer architecture, IoT systems, security, artificial intelligence and machine learning, edge intelligence, data analytics, autonomous systems, and others that are of interest to ISVLSI participant, are encouraged to apply. We also invite multi-partner European projects to propose a special session to present their project and its most recent outcomes.

- Special sessions typically run for 1.5-2 hours.
- Each speaker at a special has to submit a 1-page extended abstract for inclusion in the conference proceedings.
- In addition to the extended abstract, each speaker may submit a full paper (up to maximum 6 pages) for inclusion in the conference proceedings. The ISVLSI proceedings will be indexed in the IEEE Xplore Digital Library.
- All papers/extended abstracts will be reviewed before being published in the official proceedings of the conference in a separate track.
- Special session organizers are welcome to present their own research in their organized sessions.
- The same online submission system (<https://edas.info/97556>) will be used for the review of the proposals as well as individual papers.
- Detailed submission instructions will be provided upon acceptance of the proposal.

Submission Guidelines:

Prepare a single PDF file (not exceeding three pages) for the proposal, where you

- describe the session topic, the format, and intended audience, and
- include a list of speakers and their biographical sketch, as well as presentation titles and short abstract.

Send your special session proposals on EDAS. For questions regarding the special sessions please contact:

- **Georgios Keramidas**, Aristotelian University of Thessaloniki, gkeramidas@csd.auth.gr
- **Nima TaheriNejad**, TU Wien, nima.taherinejad@tuwien.ac.at
- **Michael Huebner**, Brandenburg University of Technology, michael.huebner@b-tu.de

Important Dates for Special Sessions

Proposal Submission Deadline: ~~March 25th, 2022~~ EXTENDED: May 8, 2022 (FINAL)
Special Session Final Description: Contact Special Session Chairs for details
Special Session Paper Submission: Contact Special Session Chairs for details
Paper Camera Ready Submission: Contact Special Session Chairs for details

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- Muhammad Shafique, NYU Abu Dhabi
- Hai (Helen) Li, Duke University

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