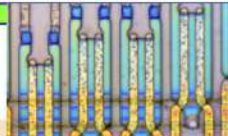# IEEE COMPUTER SOCIETY
# ANNUAL SYMPOSIUM ON VLSI

ISVLSI 2026

## Call for Papers
## www.ieee-isvlsi.org

### July 7 -10, 2026
### Kolkata, India

The Symposium explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design and system-on-chip issues, to bringing VLSI methods to new areas and technologies like nano- and molecular devices, hardware security, etc. ISVLSI 2026 highlights the theme of Next-Gen ICs and Beyond: Empowering AI, IoT, and Chiplets. Future design methodologies are also one of the key topics at the Symposium, as well as new EDA tools to support them. Over three decades the Symposium has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry. The Proceedings are published by IEEE Xplore. The Symposium has established a reputation in bringing together well-known international scientists as invited speakers; the emphasis on high quality will continue at this and future editions of the symposium.

## Contributions are sought in the following areas

Emerging Trends in VLSI, Nanoelectronics, High Power Devices and its application, Molecular, Biological and Quantum Computing, VLSI Circuits and Systems, FPGA Based and Reconfigurable Systems, System Design and Security, SoC and Noc Design, Many Core/Multicore Designs, ASICs, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, EDA, Logic and Behavioral Synthesis, Physical Design. (See the webpage for more topics) The Symposium Program will include contributed papers and invited speakers, and also a poster session. Keynote addresses, special sessions and Student Research Forum are planned as well. Authors are invited to submit full-length, original, unpublished papers. To enable blind review, the author list should be omitted from the main document. All submissions should be in PDF format.

## Paper submission deadlines (main track)

**Submission Deadline: February 10, 2026**
**Acceptance Notification: May 10, 2026**
**Submission of Final Version: May 30, 2026**

Apart from the main conference track, authors can also submit papers in the following conference satellite sessions: Special sessions, Quantum workshop, Industry Forum Student research forum, Research Demo session, Collaborative research projects session For the submission deadlines of the satellite sessions, please check the conference website.

Selected papers will be invited for submission to a special issue on IEEE Transactions on Very Large Scale Integration (VLSI) Systems and ACM Journal on Emerging Technologies in Computing Systems. The selection process is based on reviewer feedback and quality of conference presentation.

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OBS: Full Committees are in the webpage

KOLKATA