

Call for Tutorials

IMPORTANT DATES

Tutorial Proposal Submission Deadline : **15th May 2026**

Tutorials proposal acceptance notification: **31st May 2026**

Submission of final tutorial slides: **15th June 2026**

Tutorial Duration

- **90 minute**
- **180 minute**

CALL FOR TUTORIALS

ISVLSI 2026 Organizing Committee invites proposals to be delivered in person on the day preceding the main conference. We welcome contributions from academia, industry, and research institutions. Tutorial proposals are invited in all areas related to VLSI and Embedded Systems, with particular interest in (but not limited to) the following topics: Hardware for ML and AI, Analog and Mixed Signal and RF Circuits, Sensor Interfacing Circuits and Systems, Test, Verification, and Reliability, Embedded Systems, Internet of Things (IoT), and Cyber-Physical System Design, Low Power Digital Systems, Electronic Design Automation (EDA), Hardware and Systems Security, Photonic Integrated Circuits and Optical Communication, Power and Energy Management, Emerging Technologies: In-memory/ Near-memory Computing, Neuromorphic Computing, Post-CMOS Technologies, Quantum Computing-Hardware, Algorithms, and Security, 3D IC and Advanced Packaging, Emerging Memory Technologies, Wireless Systems- 5G and Beyond, Reconfigurable Computing and Processor Design, VLSI Circuits and Systems for Manufacturing and Automotive, and Electric Vehicles (EVs). We also encourage interdisciplinary, cross-domain, and application-driven tutorial proposals.

Please submit tutorial proposals through link: <https://tinyurl.com/34nhcebe>

Required Information for Tutorial Proposals:

Tutorial Title: Provide a clear, descriptive title that reflects the tutorial's content and focus. **Tutorial Area(s):** Specify the broad domain(s) your tutorial addresses. **Learning Objectives:** Describe what participants will learn, why the topic is relevant and timely for attendees, and outline key subtopics and expected learning outcomes. **Target Audience:** Define the intended audience, such as UG/PG, researchers, industry professionals, and engineers, and explain who will benefit most from the tutorial. **Prerequisite Knowledge:** Specify the background knowledge required (introductory, intermediate, or advanced), including any foundational topics or tools attendees should be familiar with. **Abstract:** Provide a concise yet informative summary (up to **1000 words**) detailing the tutorial's purpose, scope, and value to the VLSI community. **Lead Speaker:** Include a one-page professional biography highlighting the lead speaker's expertise, relevant experience, and notable contributions to the field. **Additional Speakers (if applicable):** List names, affiliations, and short bios of co-speakers. Indicate the specific sections each speaker will cover. **Tutorial Structure and Time Allocation:** Provide a detailed breakdown of the tutorial agenda, listing topics and approximate time allocations. Optional components like Q&A sessions, live demos, or panel discussions may be included. **Presentation Format:** Tutorials must be delivered in person. Hybrid or virtual formats will not be considered. **Content Scope:** Submissions should adopt a broad perspective. Proposals that focus exclusively on the presenters' own work, tools, or platforms are discouraged. **Evaluation Criteria:** Relevance to the audience, Clarity and organization, Educational value, Demonstrated expertise of the presenter(s) and Inclusion of diverse perspectives and approaches.